

General Description

The TP4056 is a complete constant-current/constant-voltage linear charger for single cell lithium-ion batteries. Its ESOP8 package and low external component count make the TP4056 ideally suited for portable applications. Furthermore, the TP4056 is specifically designed to work within USB power specifications. No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge voltage is fixed at 4.2V, and the charge current can be programmed externally with a single resistor. The TP4056 automatically terminates the charge cycle when the charge current drops to 1C/10th the programmed value after the final float voltage is reached. When the input supply (wall adapter or USB supply) is removed, the TP4056 automatically enters a low current state, dropping the battery drain current to less than 1µA. Other features include charge current monitor, automatic recharge and a status pin to indicate charge termination and the presence of an input voltage.

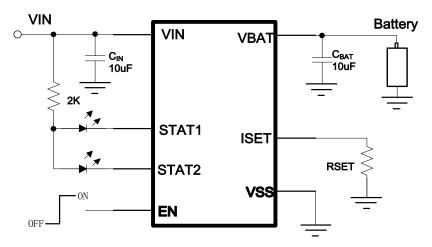
Features

- ■Programmable charging current 1000mA
- No need for external MOSFET, detection resistor and isolation diode
- For single cell lithium batteries, using ESOP8 packaging for complete linear charging
- Constant current/constant voltage operation with thermal regulation function that maximizes charging rate without overheating hazard
- Charging current monitor output for battery level detection
- Automatic Recharging
- Charging status dual output, no battery and fault status display
- C/10 charging termination
- ■The static current in shutdown mode is 35uA
- ■2.9V trickle charging
- Battery temperature monitoring
- Soft start limits surge current
- BAT input anti reverse protection
- Can be activated at 0V

Applications

- Portable Media Players/MP3 players
- Cellular and Smart mobile phone
- PDA/DSC
- Bluetooth Applications

Typical Application Circuit



Among them, the dashed box indicates the R1/R2/NTC resistor part for battery temperature monitoring, which is optional. Alternatively, the TEMP pin can be directly grounded without monitoring the battery temperature.



1A Standalone Linear Li-Ion Battery Charger Functional Pin Description

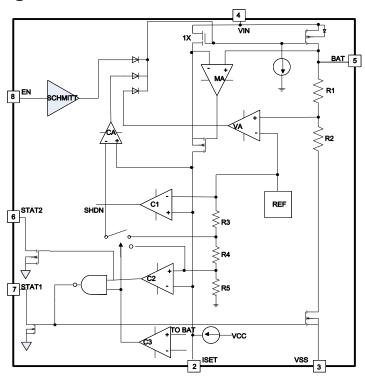
Package Type	Pin Configurations			
ESOP-8	NC 1			

Pin Description

PIN	PIN Number	DESCRIPTION		
1	NC	No Connector.		
2	ISET	Charge Current Program and Charge Current Monitor Pin. The charge current is programmed by connecting a 1% resistor, R_{ISET} , to ground. When charging in constant-current mode, this pin servos to 1V. In all modes, the voltage on this pin can be used to measure the charge current using the following formula: $IBAT=1000/R_{ISET}$		
3	VSS	VSS is the connection to system ground.		
4	VIN	VIN is the input power source. Connect to a wall adapter.		
5	BAT	BAT is the connection to the battery.		
6	STAT2	Open-Drain Charge Status Output. When the battery is charging, the STAT pin could be pulled High by an external pull high resistor. When the charge cycle is completed, the pin is pulled Low by an internal N-channel MOSFET.		
7	STAT1	Open-Drain Charge Status Output. When the battery is charging, the STAT pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, the pin could be pulled High by an external pull high resistor.		
8	EN	Chip enable pin. Charging when the pin is floating or connected to a high voltage. Discharge when the pin pull low.		



Function Block Diagram



Absolute Maximum Ratings Note 1

-	♦ Input to GND(VIN)	0.3V to 8V
	♦ BAT to GND	5V to 6V
	♦ VIN to BAT	8V
	♦ Other Pin to GND	0.3V to 8V
	♦ BAT Short-circuit Duration	Continuous
	♦ Storage Temperature	
	♦ Operating Junction Temperature Range (TJ)	
	♦ Maximum Soldering Temperature (at leads, 10 sec)	260°C
	Note 4. Observe by and those Patrick and with the Market to Market and Delicary	annut dans as to the device. These are stores

Note 1. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Thermal Information

Y	Maximum Fower Dissipa	(FD,TA=23 C)	Z V V
\Rightarrow	Thermal Resistance (JA)	5	0°C/W

ESD Susceptibility

	HBM(Human Body Mode)		2KV	
---------	----------------------	--	-----	--

211/



Electrical Characteristics

(The specifications which apply over the full operating temperature range, otherwise specifications are at TA = 25°C. VIN = 5V, unless otherwise noted.)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNITS
VIN	Adapter/USB Voltage Range		4.5	5	6.5	V
Icc	Input Supply Current	Standby Mode (Charge Terminated)		50		uA
V_{FLOAT}	Regulated Output (Float) Voltage	I _{BAT} = 40mA	4.158	4.2	4.242	V
		RISET = 10k		100		
		RISET = 2k		500		mA
I _{BAT}	BAT Pin Current	Reverse Current, VBAT=-4.2V		1		
		Standby Mode, VBAT = 4.2V Shutdown Mode (RISET NC)		1		uA
I _{TRIKL}	Trickle Charge Current	VBAT < VTRIKL		10		%IBAT
VTRIKL	Trickle Charge Threshold Voltage	RISET = 10k, VBAT Rising		2.9		V
V _{TR-HYS}	Trickle Charge Hysteresis Voltage	RISET = 10k		100		mV
Vuv	VIN Under voltage Lockout Threshold	From VIN Low to High		3.8		V
V _{UVHYS}	VIN Under voltage Lockout Hysteresis			200		mV
Vasd	VIN –VBAT Lockout Threshold Voltage	VIN from Low to High		150		mV
	Tamaia shi ara Oussant Thurada al al	RISET = 10k		40		0/10 4 T
ITERM	Termination Current Threshold	RISET = 2k		10		%IBAT
V_{ISET}	ISET Pin Voltage	RISET = 10k, Current Mode		1.0		V
VSTAT	STAT Pin Output Low Voltage	ISTAT = 5mA			0.5	V
ISTAT	STAT1/2 Pin Weak Pull-Down Current	I _{CHRG} = 5V			5	uA
ΔV _{RESTAT}	Recharge Battery Threshold Voltage	VFLOAT - VRESTAT		200		mV
T _{LIM}	Junction Temperature in Con	stant Temperature Mode		125		°C
Ron	Ron Power FET "ON" Resistance (Between VIN and BAT)			600		mΩ
V _{EN-ON}	EN Logic-High Volt	age Threshold	1.4			V
V _{EN-OFF}	EN Logic-Low Volt	age Threshold			0.4	V



Application Information

The TP4056 is a single cell lithium-ion battery charger using a constant-current/constant-voltage algorithm. It can deliver up to 1000mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of ±1%(4.2V). The TP4056 includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external current sense resistor is required; thus, the basic charger circuit requires only two external components. Furthermore, the TP4056 is capable of operating from a USB power source.

Normal Charge Cycle

A charge cycle begins when the voltage at the VIN pin rises above the UVLO threshold level and a 1% ISET ram resistor is connected from the ISET pin to ground or when a battery is connected to the charger output. If the BAT pin is less than 3V, the charger enters trickle charge mode. In this mode, the TP4056 supplies approximately 1/10 of the ISET rammed value current to bring the battery voltage up to a safe level for full current charging.

When the BAT pin voltage rises above 3V, the charger enters constant-current mode, where the ISET rammed charge current is supplied to the battery. When the BAT pin approaches the final float voltage, the TP4056 enters constant-voltage mode and the charge current begins to decrease. When the charge current drops to 1/10 of the ISET rammed value, the charge cycle ends.

ISET ramming Charge Current

The charge current is ISET rammed using a single resistor from the ISET pin to ground. The battery charge current is 1000 times the current out of the ISET pin. The ISET ram resistor and the charge current are calculated using the following equations:

 R_{SET} =1000V/ I_{BAT} , I_{BAT} = 1000V/ R_{SET}

The charge current out of the BAT pin can be determined at any time by monitoring the ISET pin voltage using the following equation:

I_{BAT}= V_{SET} x 1000/R_{SET}(TP4056)

Automatic Recharge

Once the charge cycle is terminated, the TP4056 continuously monitors the voltage on the BAT pin using a comparator with a 2ms filter time. A charge cycle restarts when the battery voltage falls below 4.0V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. CHRG output enters a strong pull-down state during recharge cycles.

Charge Status Indicator (STAT)

The charge status output has two different states: strong pull-down (~5mA) and high impedance. The strong pull-down state indicates that the TP4056 is in a charge cycle. High impedance indicates that the charge cycle complete or the TP4056 is in under voltage lockout mode: either V_{IN} is less than 150mV above the BAT pin voltage or insufficient voltage is applied to the VIN pin. A microprocessor can be used to distinguish between these two states.

Function	STAT1(pin7)	STAT2(pin6)	
Charging	Low	High	
Charge END	High	Low	

Thermal Limiting

An internal thermal feedback loop reduces the I_{SET} rammed charge current if the die temperature attempts to rise above a preset value of approximately 125°C. This feature protects the TP4056 from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the TP4056.

The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions.



Power Dissipation

The conditions that cause the TP4056 to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated by the internal MOSFET—this is calculated to be approximately:

PD=(VIN-VBAT) • IBAT

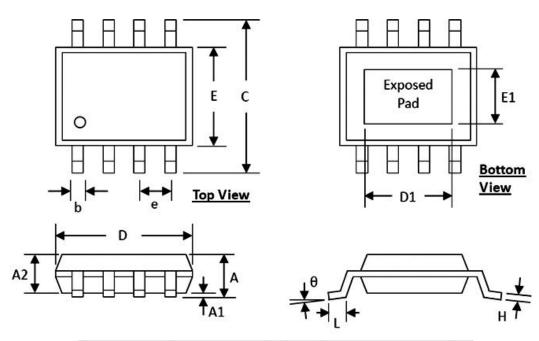
where PD is the power dissipated, VIN is the input supply voltage, VBAT is the battery voltage and IBAT is the charge current. The approximate ambient temperature at which the thermal feedback begins to protect the IC is:

TA=125°C-PD θ JA

Ta=125°C -(VIN-VBAT) • IBAT • θ JA



Packaging Information



SYMBOLS	DIMENSION (MM)		DIMENSION (INCH)	
STIVIBULS	MIN	MAX	MIN	MAX
Α	1.30	1.70	0.051	0.067
A1	0.00	0.15	0.000	0.006
A2	1.25	1.52	0.049	0.060
b	0.33	0.51	0.013	0.020
С	5.80	6.20	0.228	0.244
D	4.80	5.00	0.189	0.197
D1	3.15	3.45	0.124	0.136
E	3.80	4.00	0.150	0.157
E1	2.26	2.56	0.089	0.101
e	1.27 BSC		0.050 BSC	
Н	0.19	0.25	0.0075	0.0098
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°